

ARM[®] Cortex[®]-M
32-bit Microcontroller

NuMicro[®] Family
NUC122 Series BSP
Revision History

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Revision 3.00.006 (Released 2024-06-21)

1. Used "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
2. Added timeout handler for infinite loop.
3. Enable LVR in SYS_PowerDown_MinCurrent Sample code to prevent power on/off fail.
4. Updated USB HID keyboard sample code to support LED status.
5. Added SYS_PowerDown_MinCurrent sample code.
6. Updated SPI_Loopback sample code.
7. Modified HID polling interval - bInterval to 10.
8. Added I2C hang up & recover mechanism for I2C Master and Slave sample code.
9. Fixed UART TX FIFO control issue in USB Sample code.

Revision 3.00.005 (Released 2021-01-20)

1. Modified to pass USB-IF CV-Chapter 9 & Class test of all USB Sample code.
2. Added Apache-2.0 license declaration in driver source.
3. Added README.md file.

Revision 3.00.004 (Released 2019-11-11)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Fixed PWM_DisableCaptureInt of PWM driver.
4. Fixed CLK_SetHCLK() bug of CLK driver.
5. Fixed CLK_EnablePLL() wrong PLL default setting value of CLK driver.

Revision 3.00.003 (Released 2017-10-24)

1. Fixed PLL clock source selection bug in CLK_SetCoreClock().
2. Fixed clear Receive Line Status interrupt flag bug in UART_ClearIntFlag().
3. Disable debug message when enabling semihost without NuLink connection.
4. Added CLK_SysTickLongDelay() for long delay.

Revision 3.00.002 (Released 2015-07-24)

1. Fix the the reset vector handler to Reset_Handler of all sample codes.
2. Fix maximum USB endpoint from 8 to 6 in USB driver, because of NUC122 USB Endpoint number is 6 only.
3. Fix UART transmit data bug in UART_TEST_HANDLE() of UART_TxRx_Function sample code.
4. Fix FMC_Erase() ISPFF flag clear to avoid ISP disable when error in FMC driver.
5. Remove APUEN enable or disable macro in FMC driver. NUC122AN doesn't support this function.
6. Remove ISPATA, VECMAP, UID, UCID from FMC driver, because they are not supported in NUC122.
7. Revise the following four macro definitions in SPI driver to avoid affecting another SPI_SS pin. SPI_SET_SS0_HIGH() SPI_SET_SS1_HIGH() SPI_SET_SS0_LOW() SPI_SET_SS1_LOW()
8. Update USB driver for better compatibility
9. Add USB_Billboard sample code to show the implementation of Billboard class.

10. Add Hard_Fault_Sample to show how to implement hard fault handler.
11. Add non-block printf supporting in retarget.c
12. Add UART FIFO size constants definitions with UART driver.
13. Add new function to control systick and select systick clock source, CLK_EnableSysTick() and CLK_DisableSysTick() in CLK driver.
14. Add UART_Wakeup sample code to demonstrate how to wakeup system by UART

Revision 3.00.001 (Released 2015-01-15)

1. Update to support new driver API

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